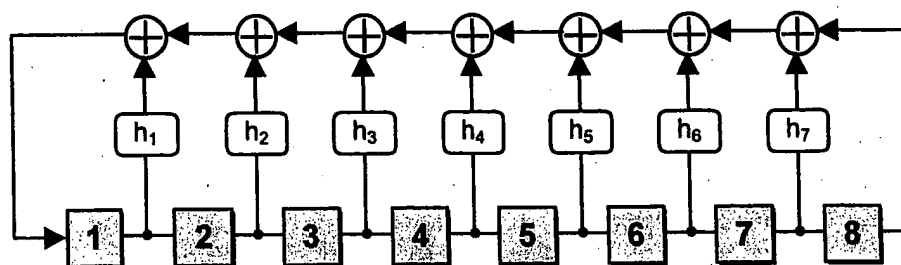


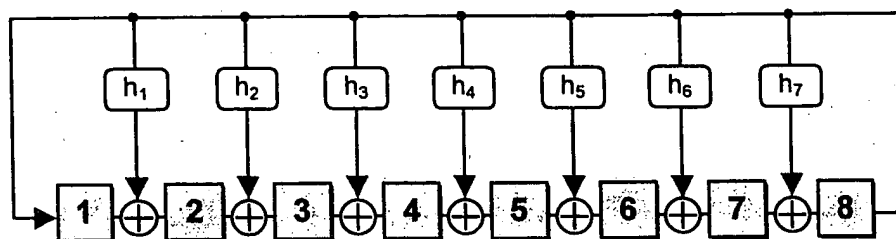
### TYPE I LFSR



PRIOR ART

FIG. 1

### TYPE II LFSR



PRIOR ART

FIG. 2

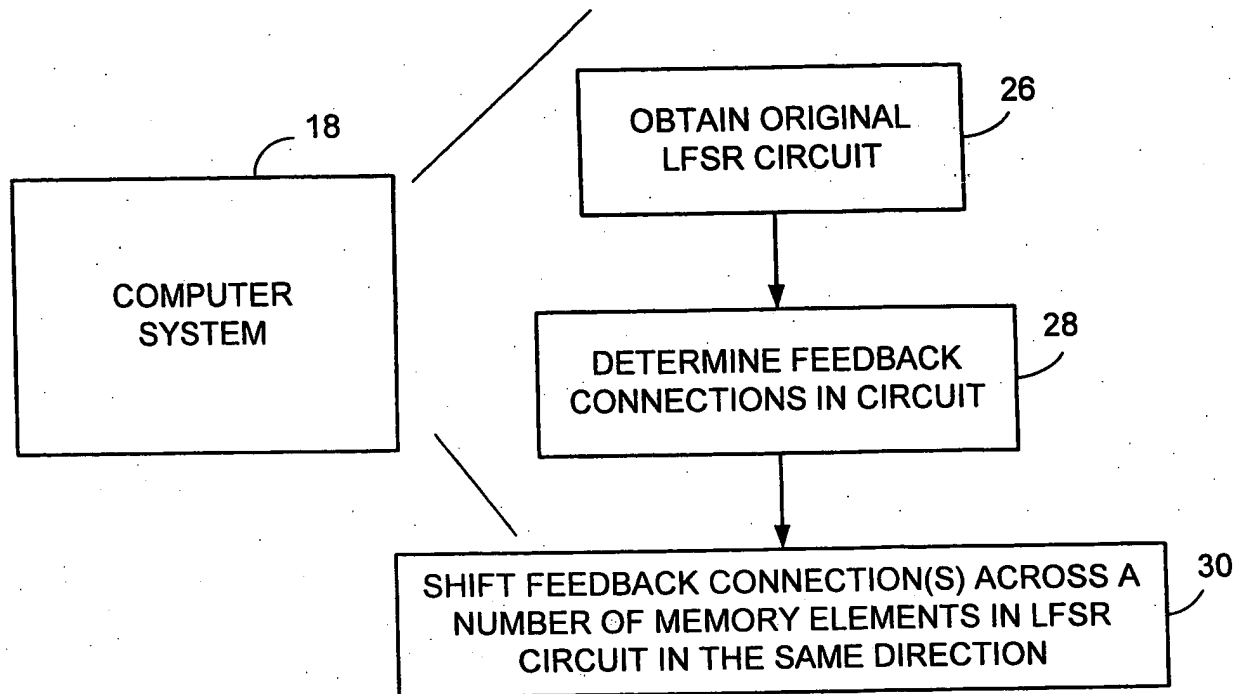


FIG. 3

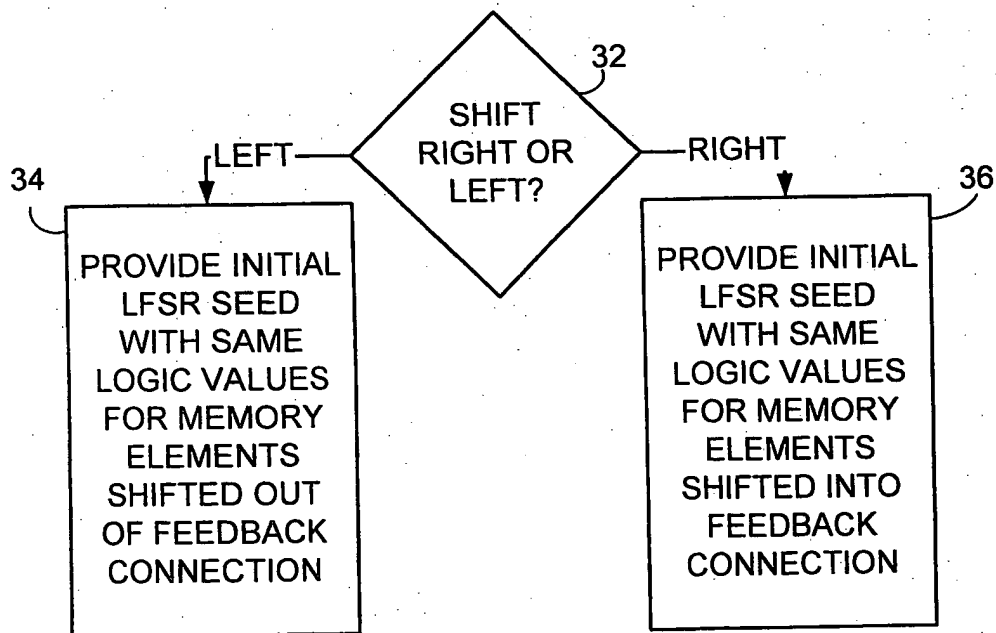


FIG. 5

FIG. 4A

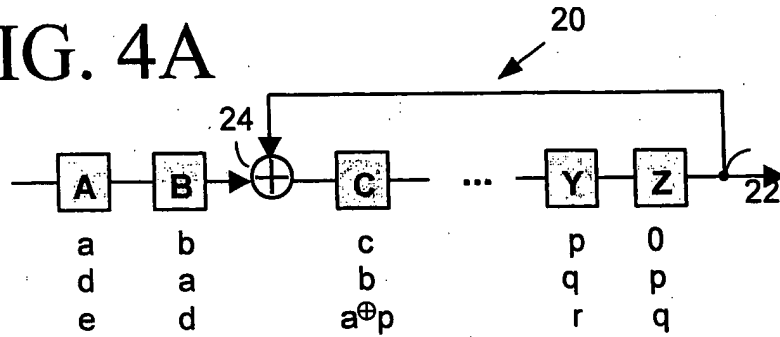


FIG. 4B

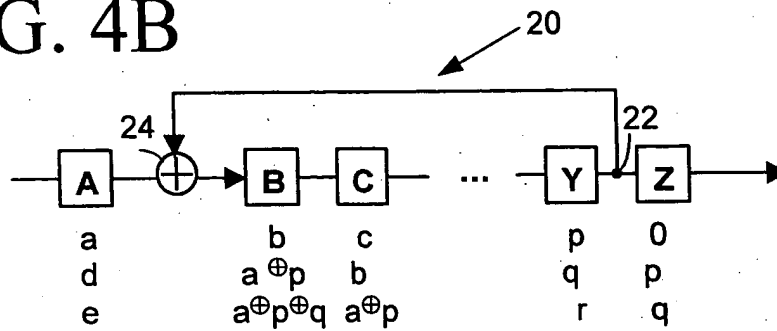


FIG. 6A

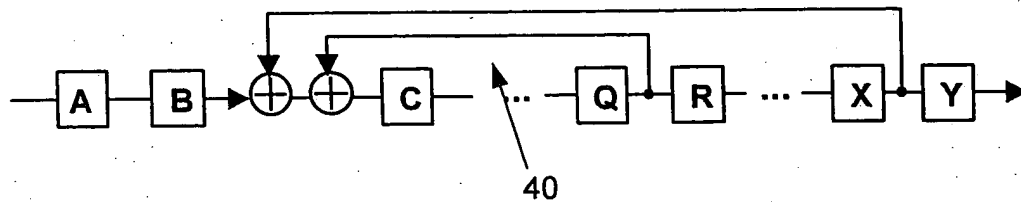


FIG. 6B

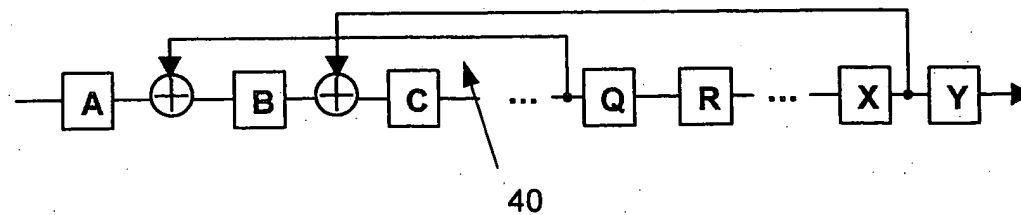


FIG. 7A

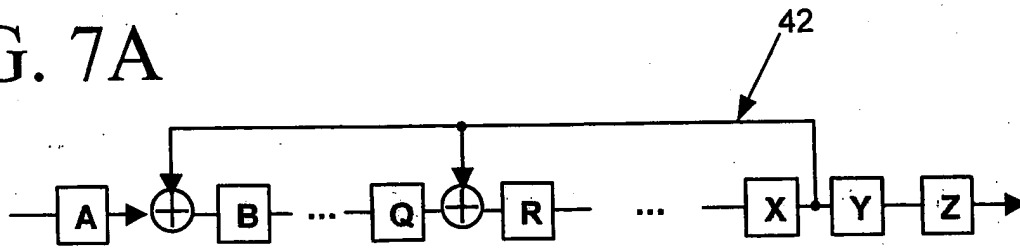


FIG. 7B

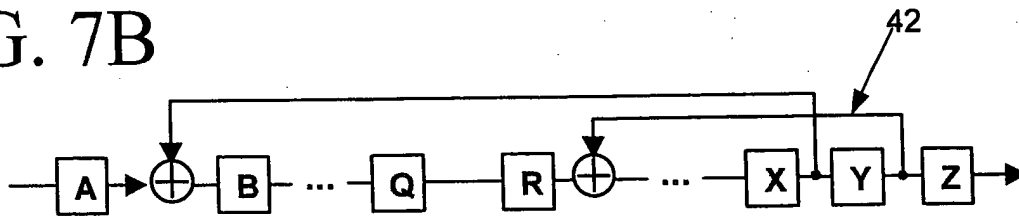


FIG. 9A

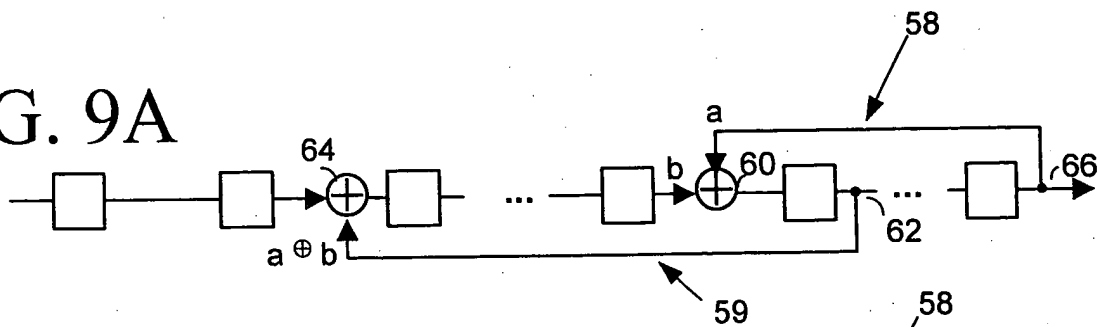


FIG. 9B

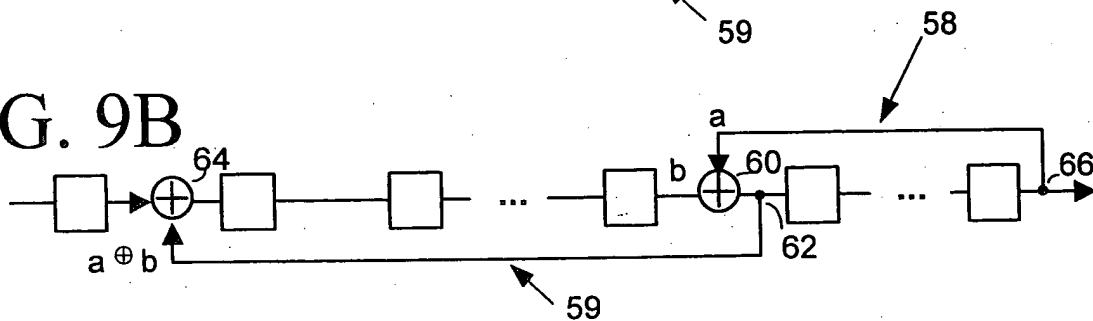
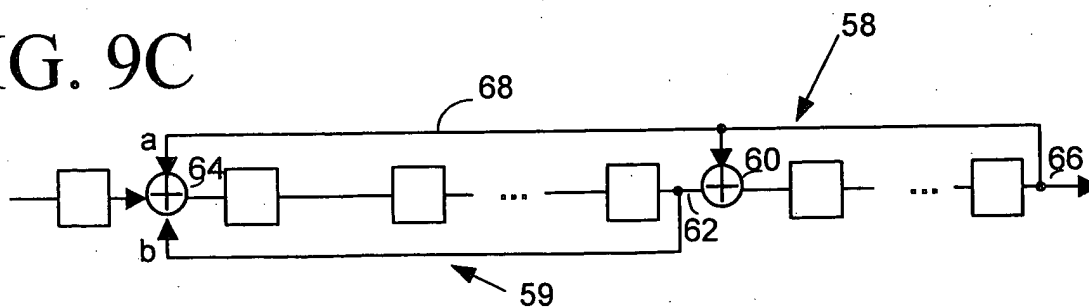


FIG. 9C



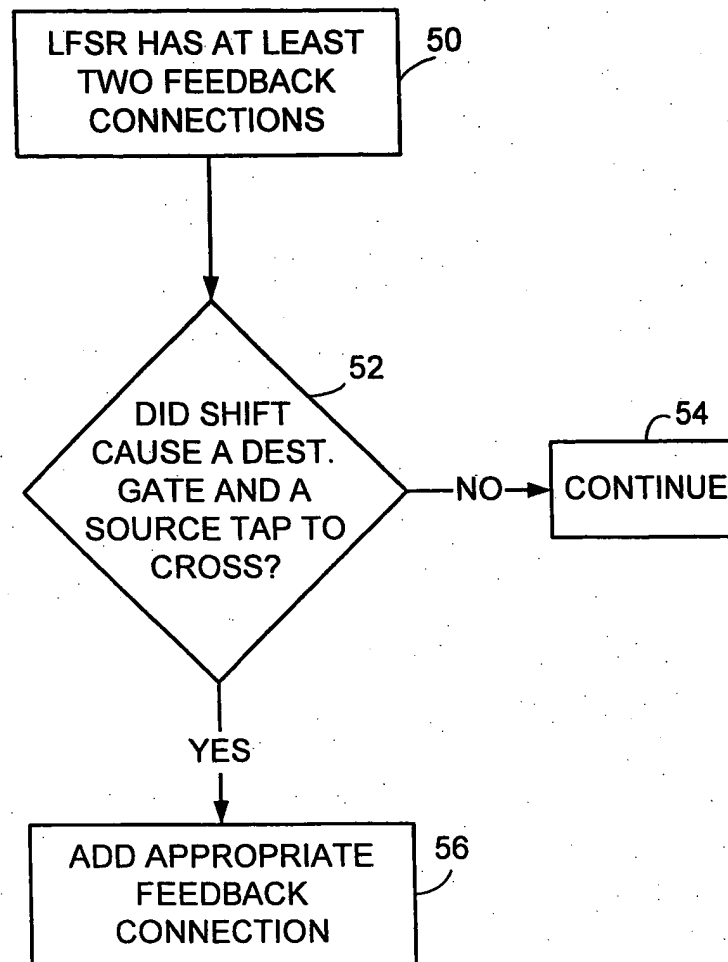


FIG. 8

Our Ref. No. 1011-67627  
For: METHOD FOR SYNTHESIZING  
LINEAR FINITE STATE MACHINES  
Inventors: Rajski et al.  
Express Mail No. EV331580780US  
**Mailed: February 17, 2004**

FIG. 10A

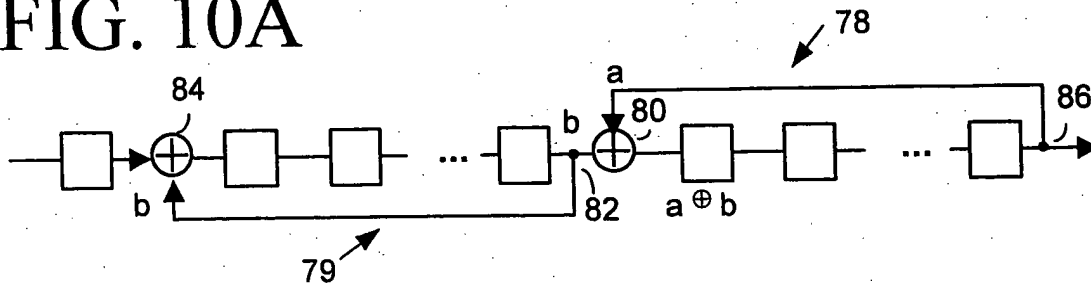


FIG. 10B

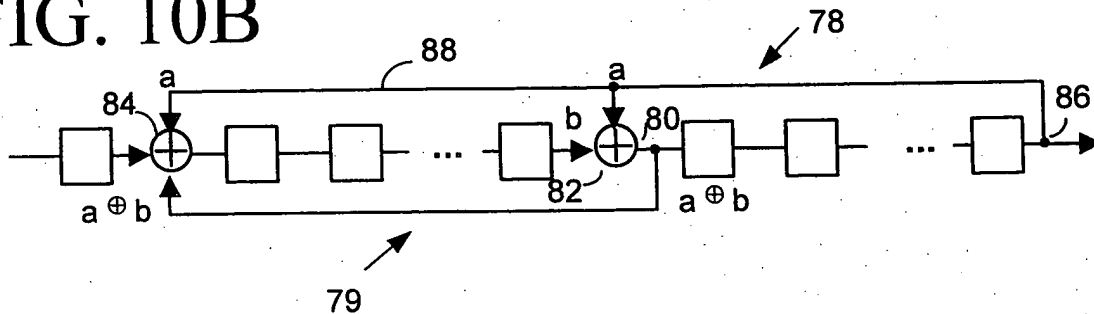


FIG. 10C

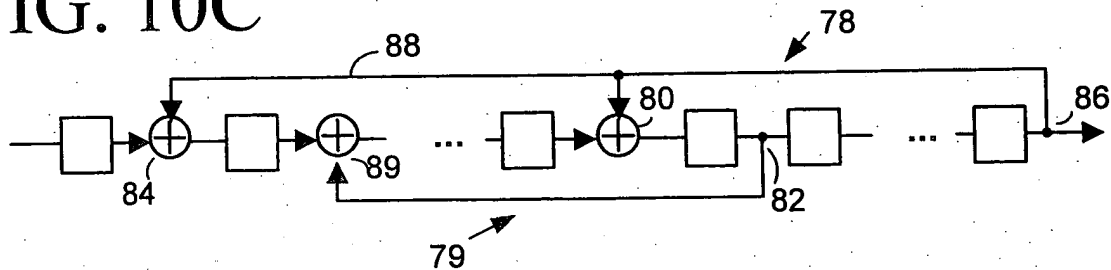


FIG. 11A

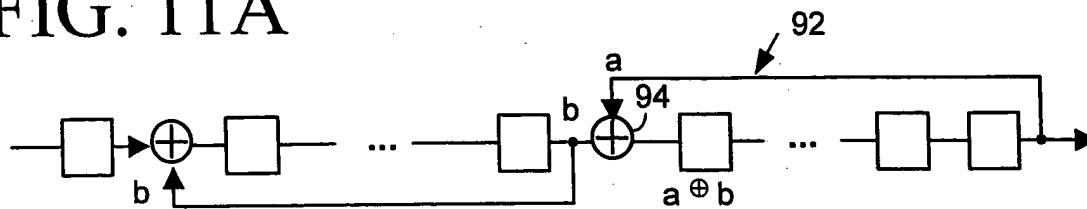


FIG. 11B

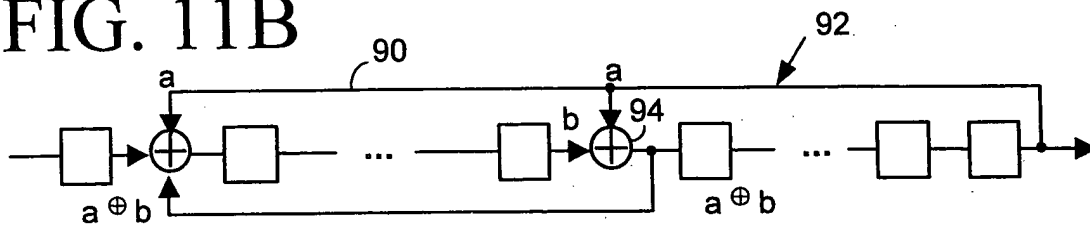


FIG. 11C

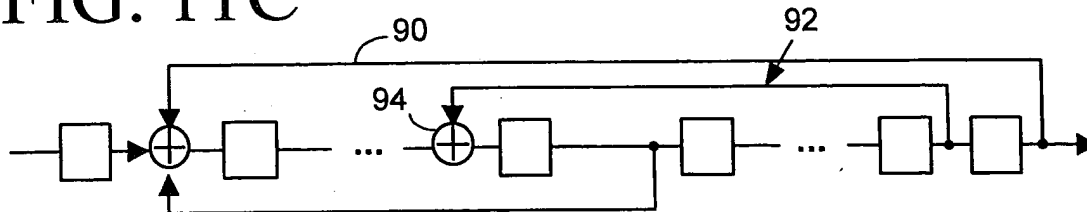


FIG. 12A

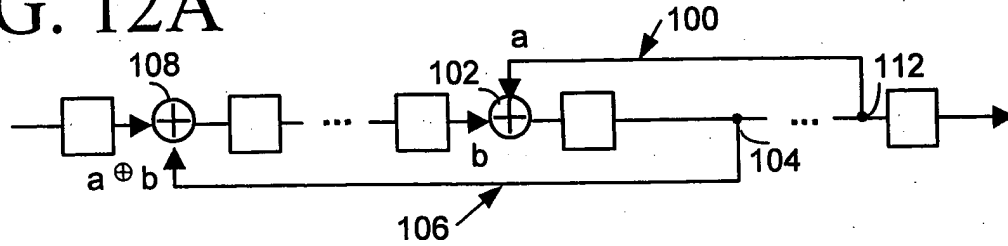


FIG. 12B

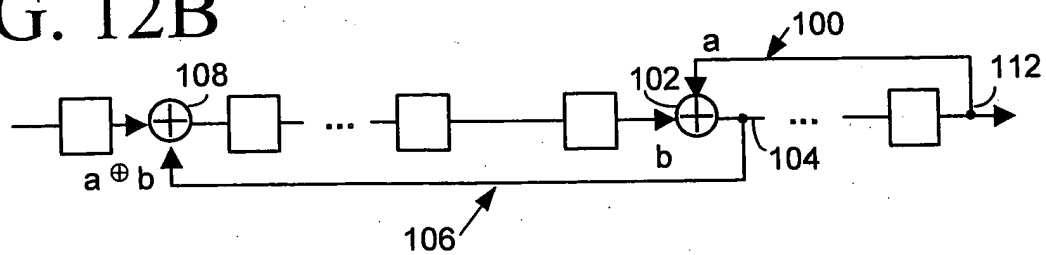
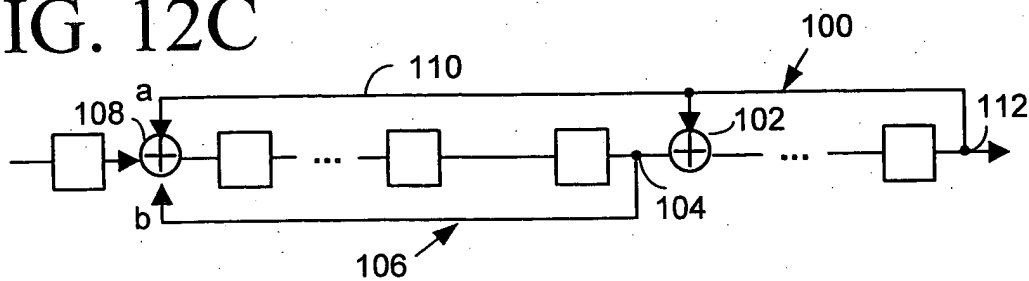


FIG. 12C





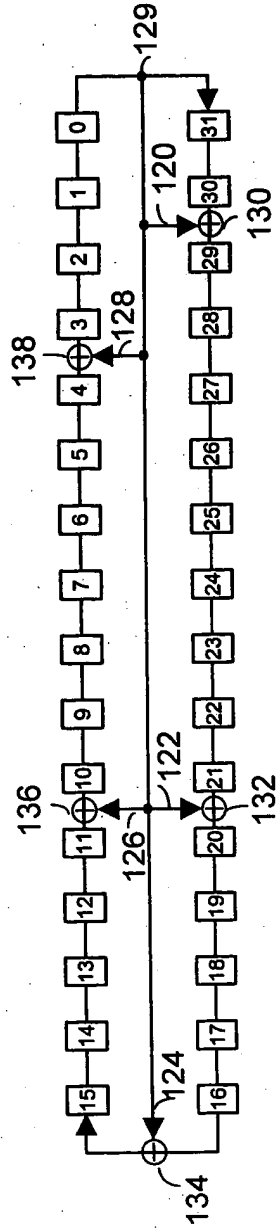


Fig. 13A

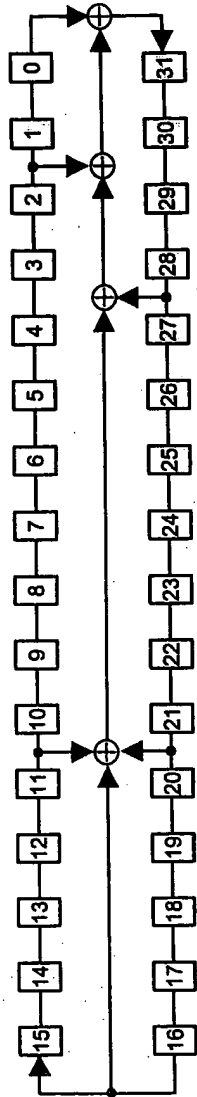


Fig. 13B

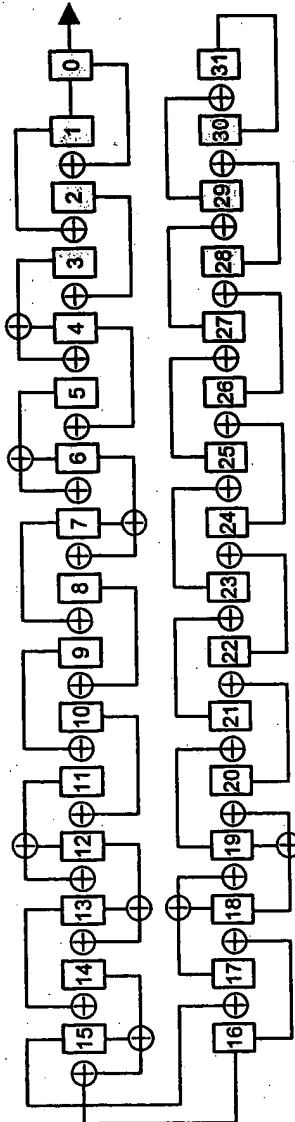


Fig. 13C

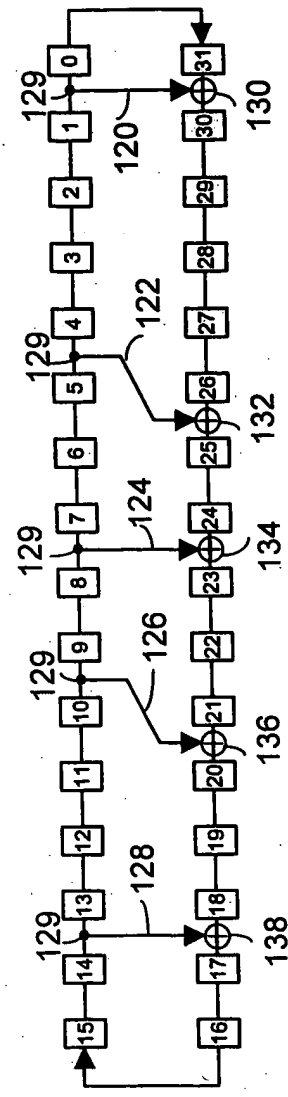


Fig. 13D

FIG. 14A

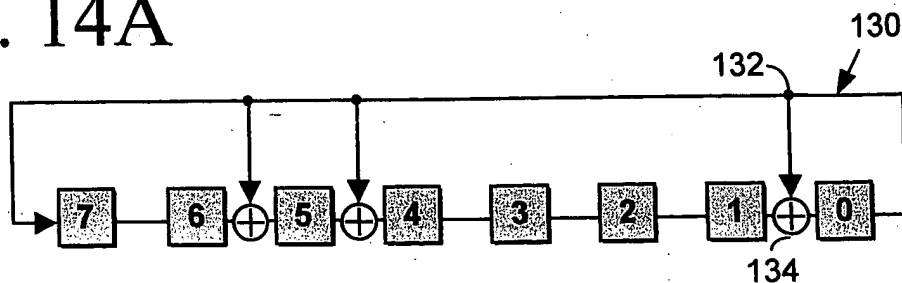


FIG. 14B

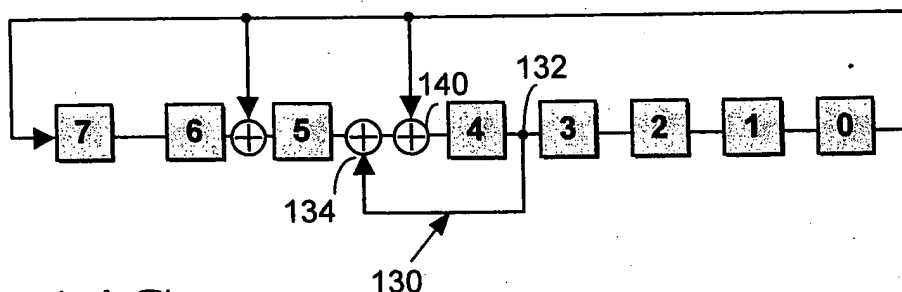


FIG. 14C

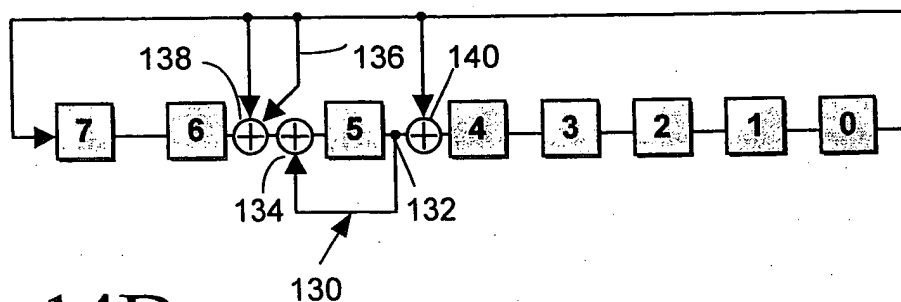


FIG. 14D

